

Fig. 1  
(Prior Art)

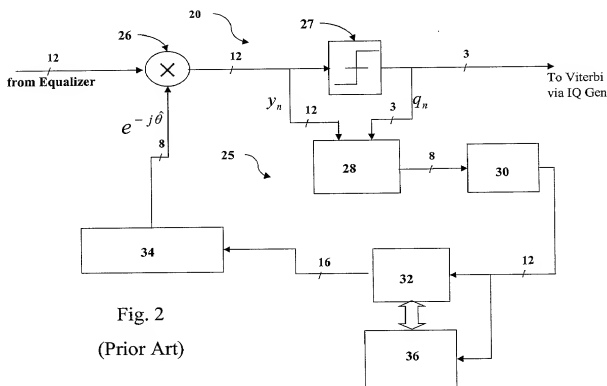


Fig. 2  
(Prior Art)

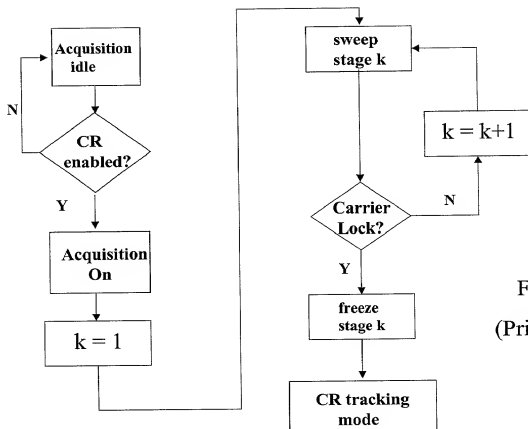


Fig. 3  
(Prior Art)

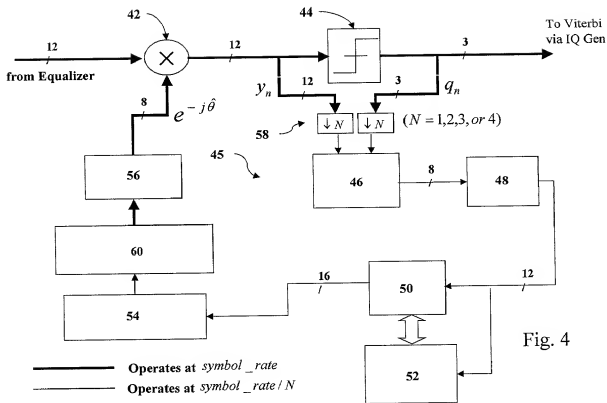


Fig. 4

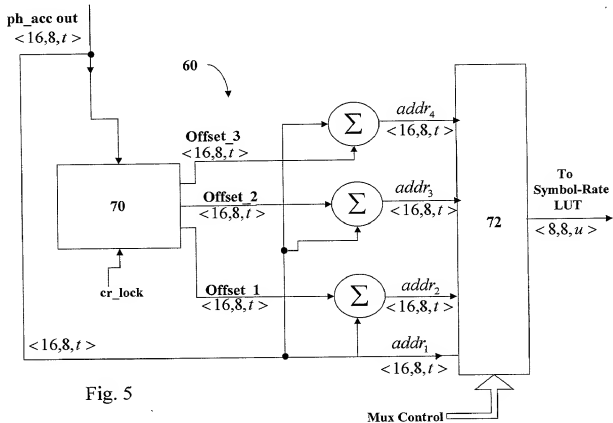


Fig. 5

Mux Control

Fig. 6

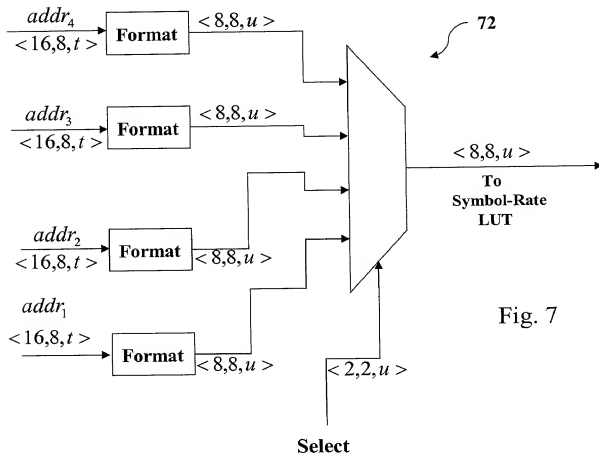
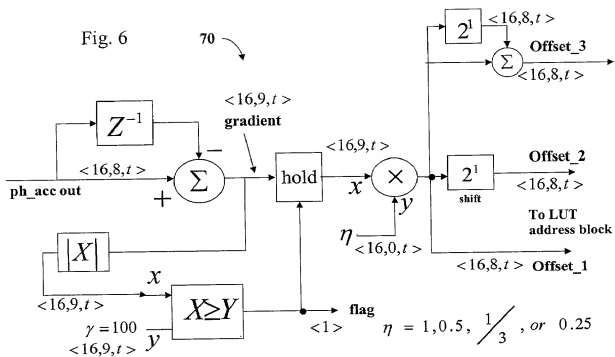


Fig. 7

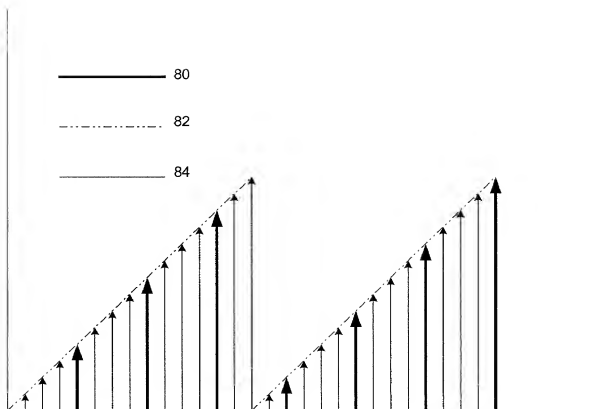


Fig. 8